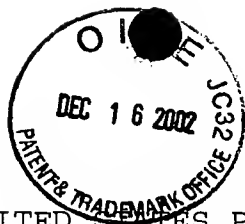


L&L-I0178



*of formal
drawings
shelved
1-2-03*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Lothar Risch et al.
Appl. No. : 09/996,279
Filed : November 28, 2001
Title : Double Gate MOSFET Transistor and Method
for the Production Thereof
Examiner : Shrinivas H. Rao Group Art Unit: 2814


REQUEST TO APPROVE DRAWING CHANGES

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Please approve the enclosed formal copies of the changed
drawings (Figs. 2-5).

Respectfully submitted,



For Applicant
VRP:cgm

Ven R. Ponugoti
Reg. No.51,052

Date: November 12, 2002
Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

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